



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OZAWA, Hiroyuki et al.**

Group Art Unit: 2815

Serial No.: **09/994,753**

Examiner: **CLARK, SHEILA V.**

Filed: **November 28, 2001**

P.T.O. Confirmation No.: 8648

For. **SEMICONDUCTOR INTEGRATED CIRCUIT WITH DUMMY PATTERNS**

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
Washington, D.C. 20231

Date: November 5, 2002

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated July 5, 2002 for one month from October 5, 2002 to November 5, 2002.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340. Two copies of this paper are enclosed herewith.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP

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